## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re	Application Of	)		
	Van Taleanahi at al	)	Group Art Unit:	2827
	Ken Takeuchi et al.	)	Examiner:	Н. Но
Serial	No.: 10/073,999	j		
Filed:	February 14, 2002	) ) )	Confirmation No.	9741
For:	Nonvolatile Semiconductor Memory Having Plural Data Storage Portions For A Bit Line Connected To Memory Cells	) ) )	Atty No.	001701.00140
IS Pa	tent and Trademark Office			

U.S. Patent and Trademark Office Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

## **Amendment**

The paper is being filed in response to the office action of July 28, 2006. If any fees are required for this paper, please charge such fees to deposit account no. 19-0733.

Listing of Claims begins on page 2 of this paper.

Remarks begin on page 6 of this paper.